

NCE N&P-Channel complementary Power MOSFET

<p>Description</p> <p>The NCE30NP4030G uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use in inverter and other applications.</p> <p>General Features</p> <table style="width:100%; border: none;"> <tr> <td style="width:50%; vertical-align: top;"> <p>N-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = 30V, I_D = 40A$ ● $R_{DS(ON)} < 9.5m\Omega @ V_{GS}=10V$ ● $R_{DS(ON)} < 23m\Omega @ V_{GS}=4.5V$ ● High Power and current handling capability ● Lead free product is acquired ● Surface mount package <p>Application</p> <ul style="list-style-type: none"> ● H-bridge ● Inverters </td> <td style="width:50%; vertical-align: top;"> <p>P-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = -30V, I_D = -30A$ ● $R_{DS(ON)} < 11m\Omega @ V_{GS}=-10V$ ● $R_{DS(ON)} < 20m\Omega @ V_{GS}=-4.5V$ </td> </tr> </table>	<p>N-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = 30V, I_D = 40A$ ● $R_{DS(ON)} < 9.5m\Omega @ V_{GS}=10V$ ● $R_{DS(ON)} < 23m\Omega @ V_{GS}=4.5V$ ● High Power and current handling capability ● Lead free product is acquired ● Surface mount package <p>Application</p> <ul style="list-style-type: none"> ● H-bridge ● Inverters 	<p>P-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = -30V, I_D = -30A$ ● $R_{DS(ON)} < 11m\Omega @ V_{GS}=-10V$ ● $R_{DS(ON)} < 20m\Omega @ V_{GS}=-4.5V$ 	<div style="text-align: center;"> <p>Schematic diagram</p> <p>Pin assignment</p> <p>DFN5X6-8L Bottom View</p> </div>
<p>N-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = 30V, I_D = 40A$ ● $R_{DS(ON)} < 9.5m\Omega @ V_{GS}=10V$ ● $R_{DS(ON)} < 23m\Omega @ V_{GS}=4.5V$ ● High Power and current handling capability ● Lead free product is acquired ● Surface mount package <p>Application</p> <ul style="list-style-type: none"> ● H-bridge ● Inverters 	<p>P-channel</p> <ul style="list-style-type: none"> ● $V_{DS} = -30V, I_D = -30A$ ● $R_{DS(ON)} < 11m\Omega @ V_{GS}=-10V$ ● $R_{DS(ON)} < 20m\Omega @ V_{GS}=-4.5V$ 		

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30NP4030G	NCE30NP4030G	DFN5X6-8L	-	-	-

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	30	-30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current	I_D	$T_C=25^\circ C$	-30	A
		$T_C=100^\circ C$	-21.2	
Pulsed Drain Current ^(Note 1)	I_{DM}	160	-120	A
Maximum Power Dissipation	P_D	35		W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150		$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	3.6	$^\circ C/W$
--	-----------------	-----	--------------

N-channel Electrical Characteristics ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

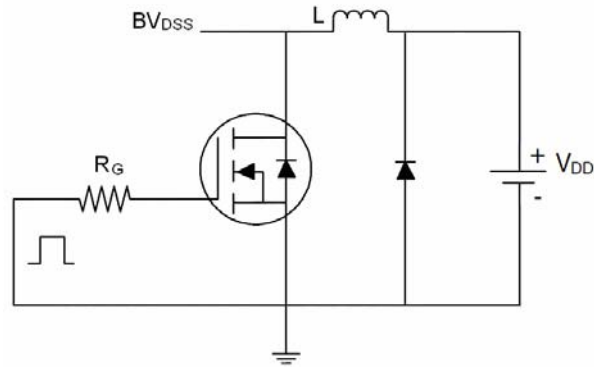
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.9	1.5	2.2	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	7	9.5	m Ω
		$V_{GS}=4.5V, I_D=20A$	-	11	23	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=20A$	26	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V,$ $F=1.0MHz$	-	1000	-	PF
Output Capacitance	C_{oss}		-	180.8	-	PF
Reverse Transfer Capacitance	C_{rss}		-	164.4	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, R_L=0.75\Omega$ $V_{GS}=10V, R_G=3\Omega$	-	5	-	nS
Turn-on Rise Time	t_r		-	12	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	19	-	nS
Turn-Off Fall Time	t_f		-	6	-	nS
Total Gate Charge	Q_g	$V_{DS}=15V, I_D=20A,$ $V_{GS}=10V$	-	17		nC
Gate-Source Charge	Q_{gs}		-	2.8		nC
Gate-Drain Charge	Q_{gd}		-	3.9		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=20A$	-		1.2	V
Diode Forward Current (Note 2)	I_S		-	-	40	A
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ C, I_F = 20A$ $di/dt = 100A/\mu s$ (Note3)	-	19	-	nS
Reverse Recovery Charge	Q_{rr}		-	10	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

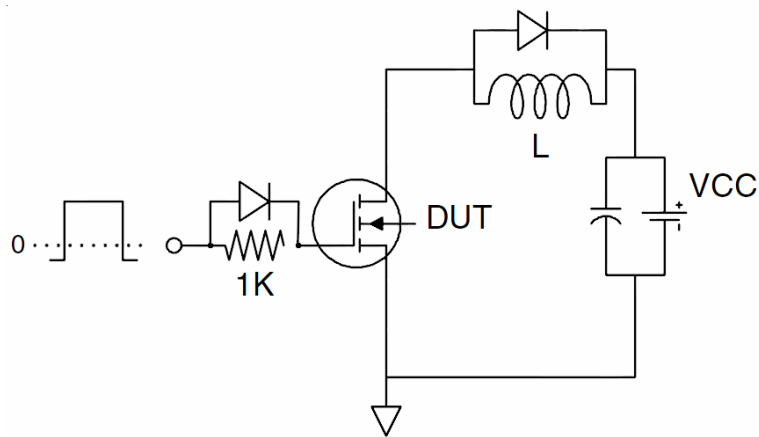
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition: $T_J=25^\circ C, V_{DD}=15V, V_G=10V, L=0.5mH, R_g=25\Omega$

Test circuit

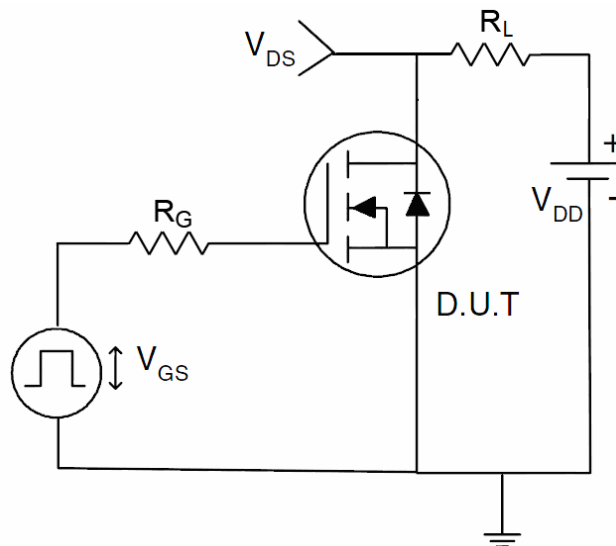
1) E_{AS} test Circuits



2) Gate charge test Circuit:



3) Switch Time Test Circuit:



N-channel Typical Electrical and Thermal Characteristics (Curves)

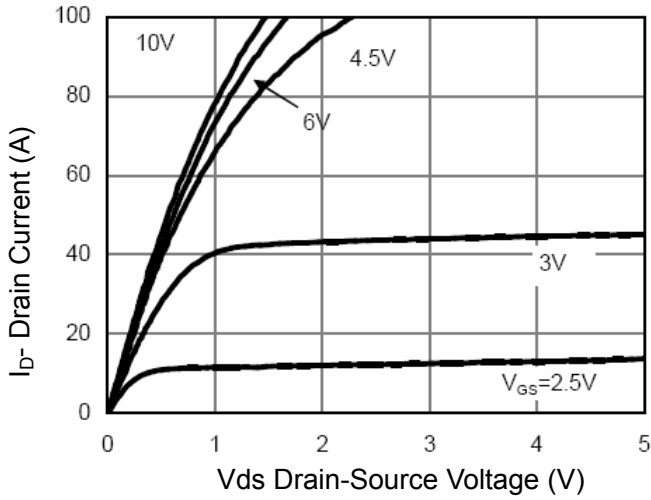


Figure 1 Output Characteristics

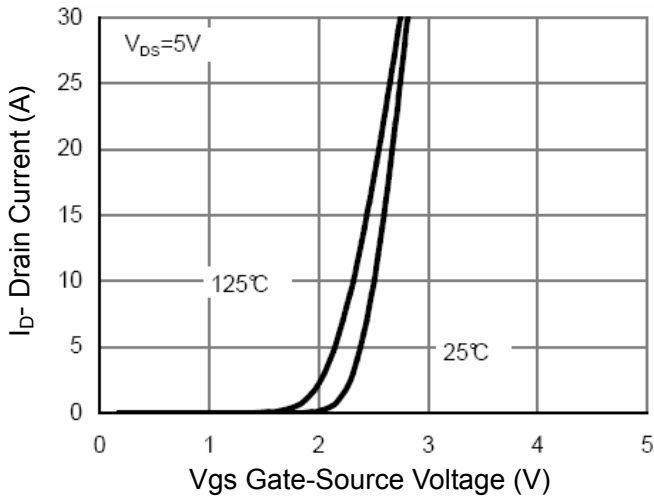


Figure 2 Transfer Characteristics

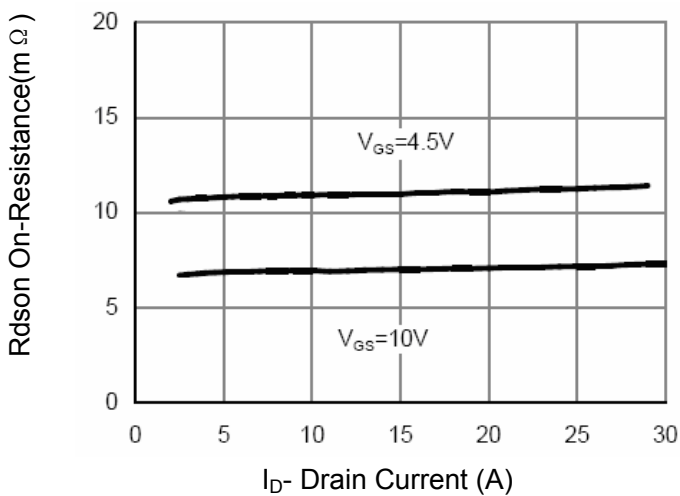


Figure 3 Rdson- Drain Current

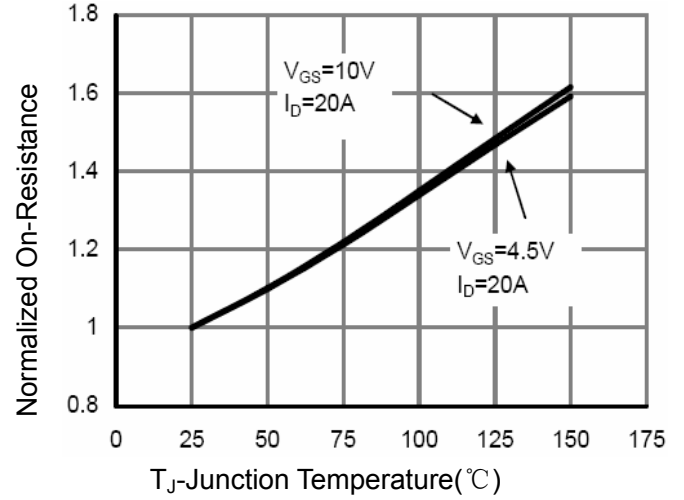


Figure 4 Rdson-Junction Temperature

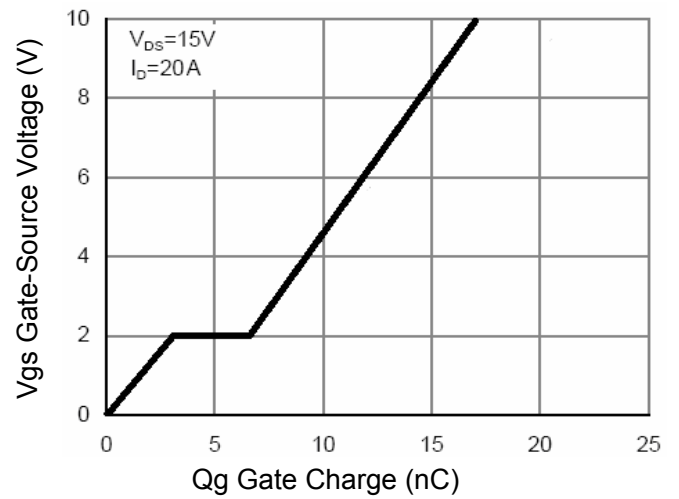


Figure 5 Gate Charge

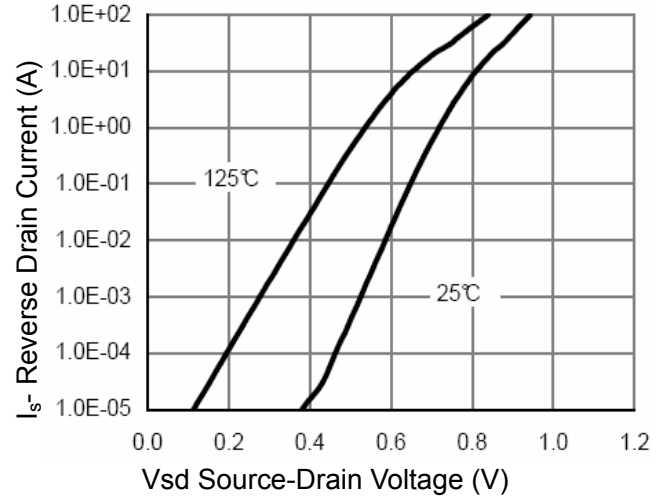


Figure 6 Source- Drain Diode Forward

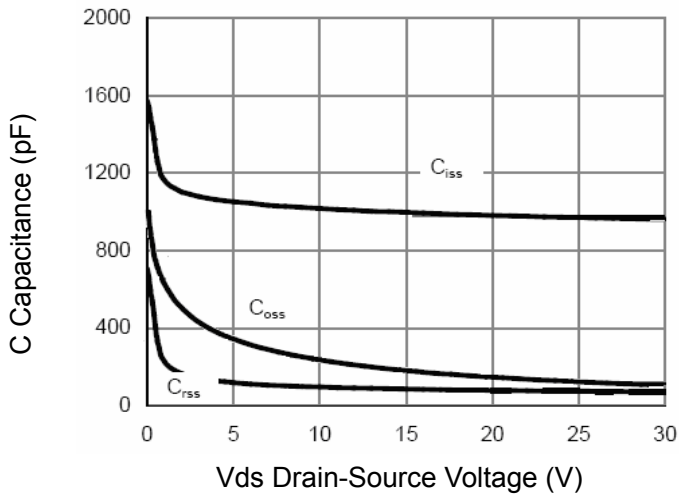


Figure 7 Capacitance vs Vds

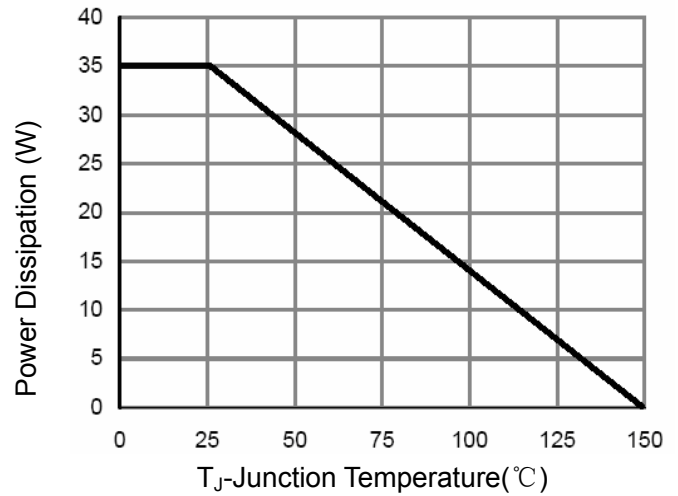


Figure 9 Power De-rating

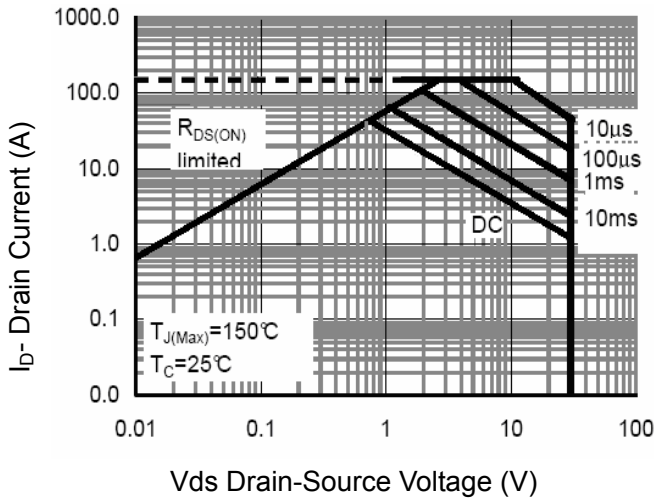


Figure 8 Safe Operation Area

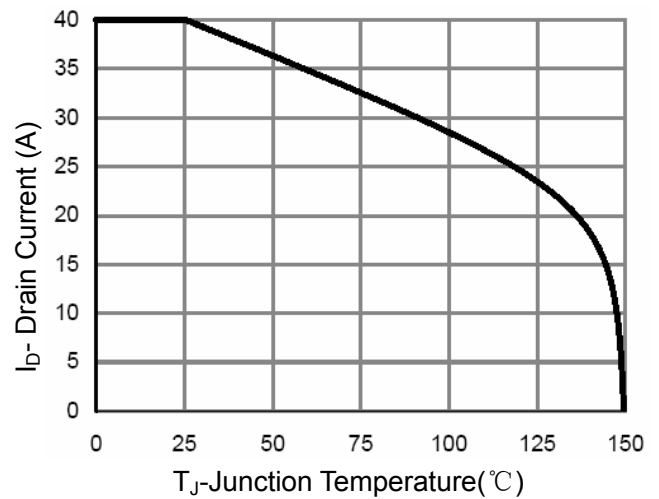


Figure 10 ID Current De-rating

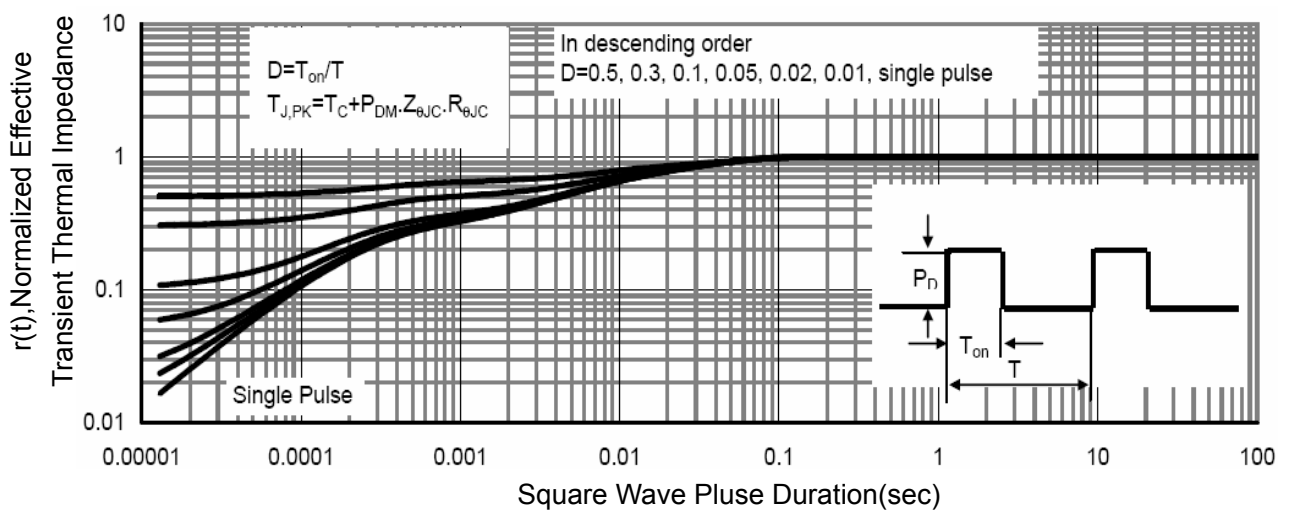


Figure 11 Normalized Maximum Transient Thermal Impedance

P-channel Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30	-33	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.6	-2.2	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-15A$	-	9	11	m Ω
		$V_{GS}=-4.5V, I_D=-15A$	-	15	20	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-15A$	15	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{ISS}	$V_{DS}=-15V, V_{GS}=0V,$ $F=1.0\text{MHz}$	-	1632	-	PF
Output Capacitance	C_{OSS}		-	227	-	PF
Reverse Transfer Capacitance	C_{RSS}		-	178	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=-15A,$ $V_{GS}=-10V, R_{GEN}=1\Omega$	-	12	-	nS
Turn-on Rise Time	t_r		-	10	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	25	-	nS
Turn-Off Fall Time	t_f		-	13	-	nS
Total Gate Charge	Q_g	$V_{DS}=-15V, I_D=-15A, V_{GS}=-10V$	-	45.6	-	nC
Gate-Source Charge	Q_{gs}		-	4.6	-	nC
Gate-Drain Charge	Q_{gd}		-	11.1	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=-15A$	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

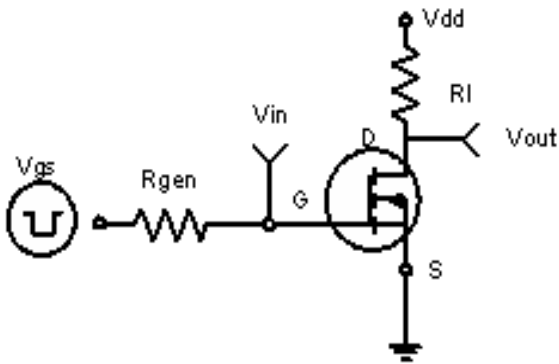


Figure 1: Switching Test Circuit

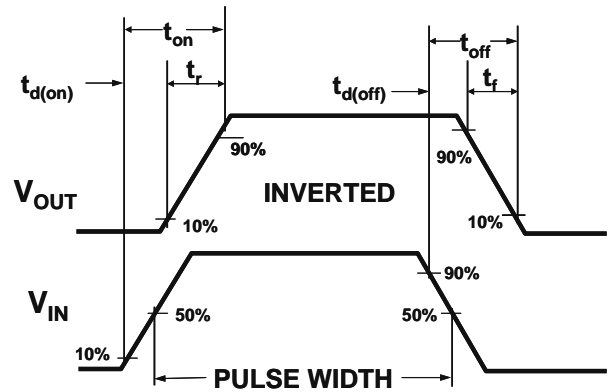


Figure 2: Switching Waveforms

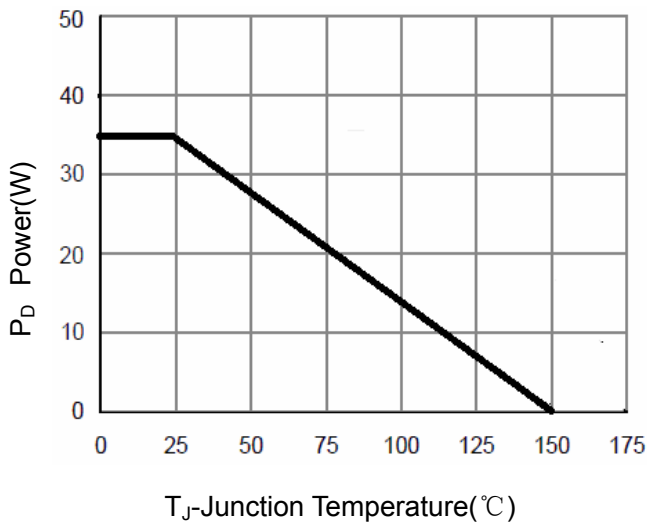


Figure 3 Power Dissipation

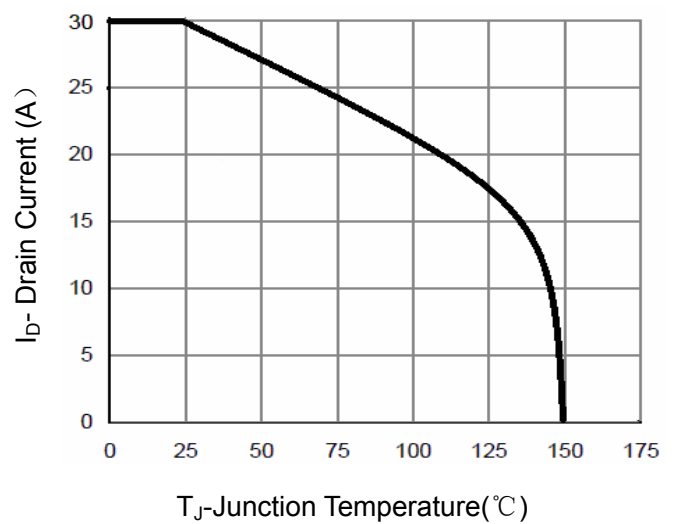


Figure 4 Drain Current

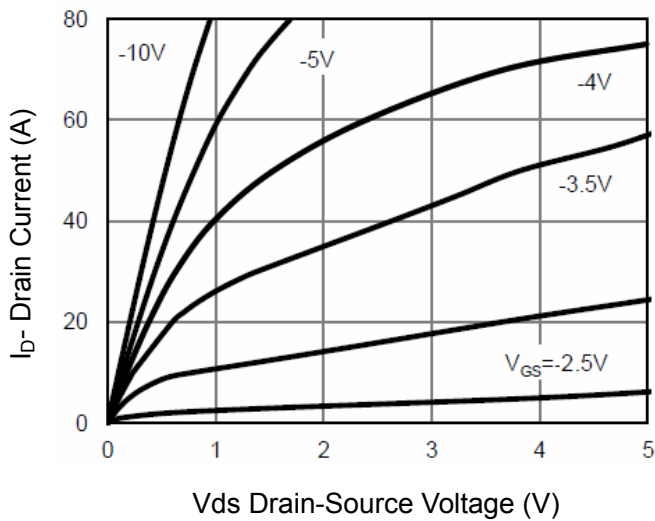


Figure 5 Output Characteristics

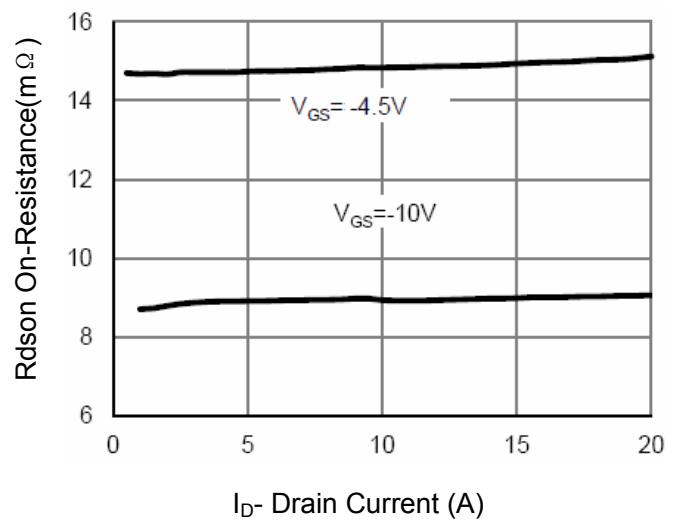


Figure 6 Drain-Source On-Resistance

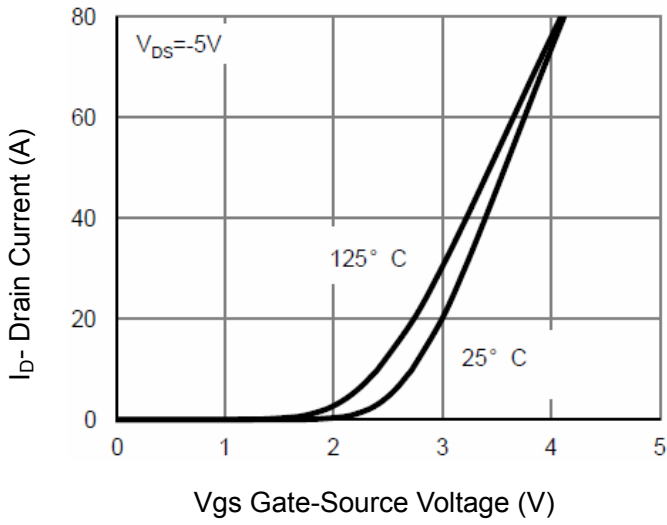


Figure 7 Transfer Characteristics

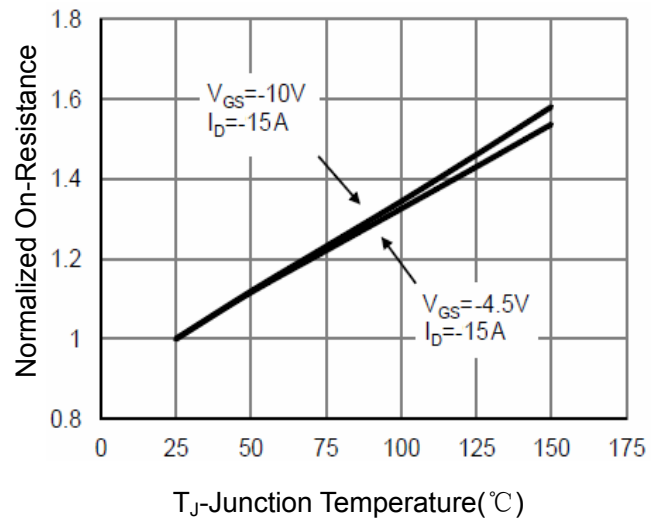


Figure 8 Drain-Source On-Resistance

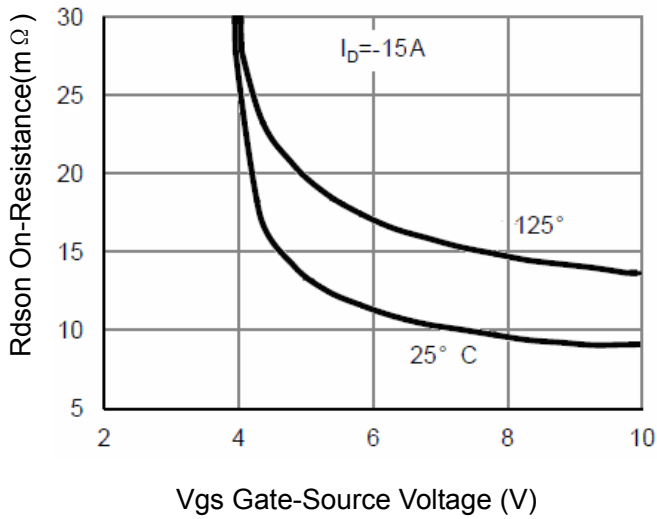


Figure 9 Rdson vs Vgs

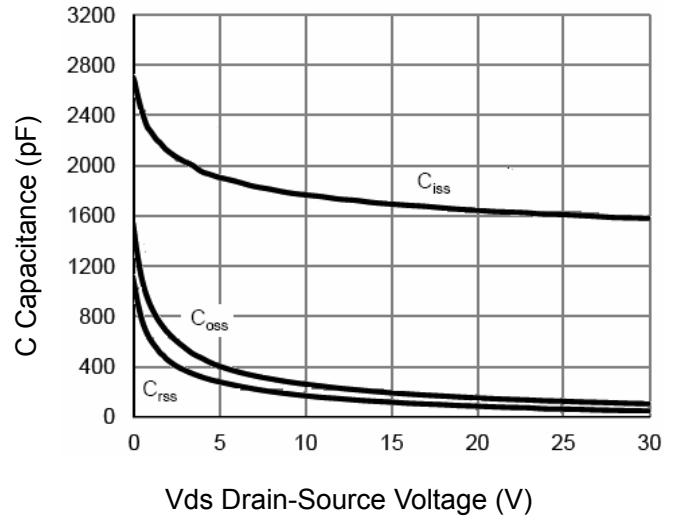


Figure 10 Capacitance vs Vds

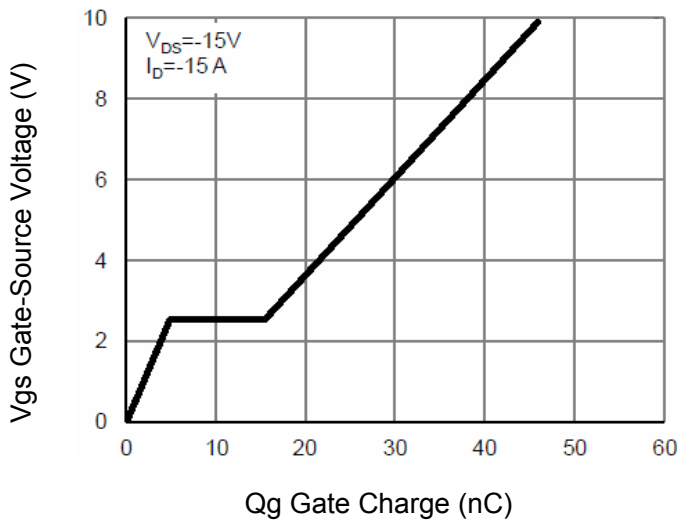


Figure 11 Gate Charge

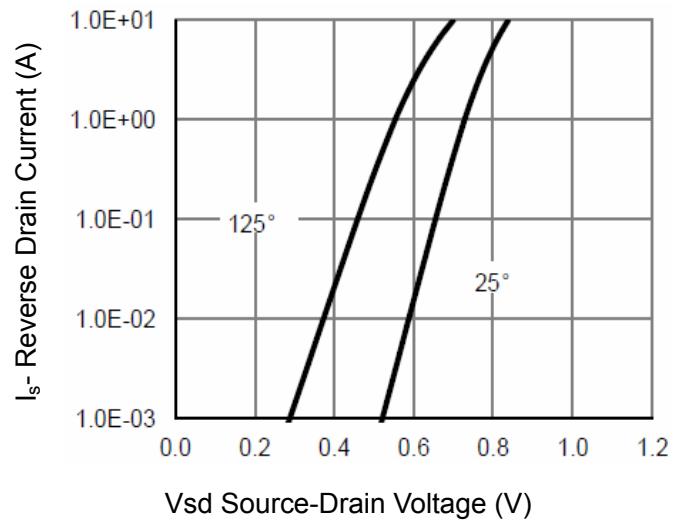
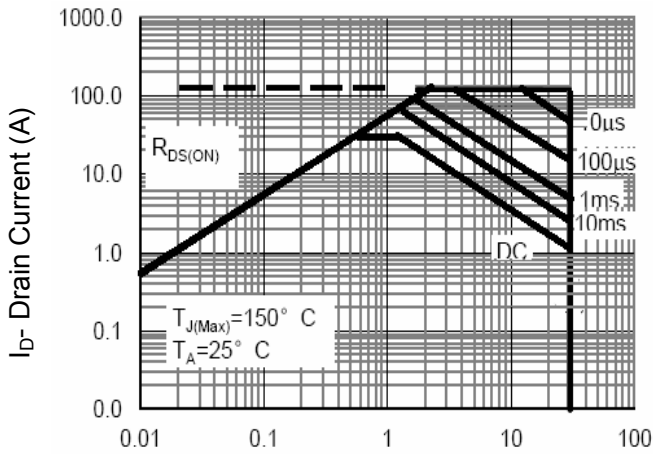
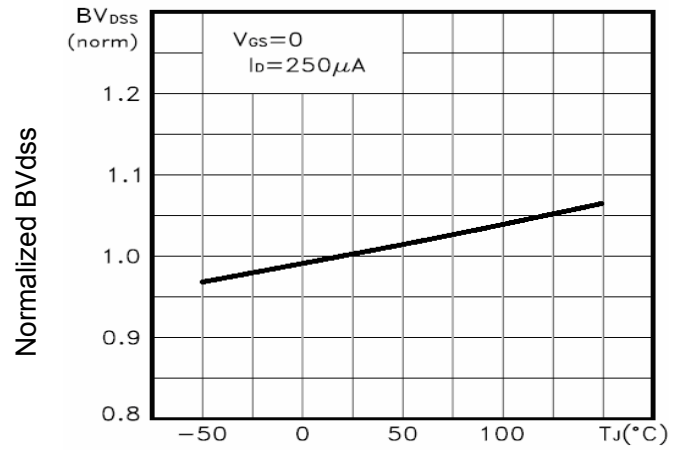


Figure 12 Source- Drain Diode Forward



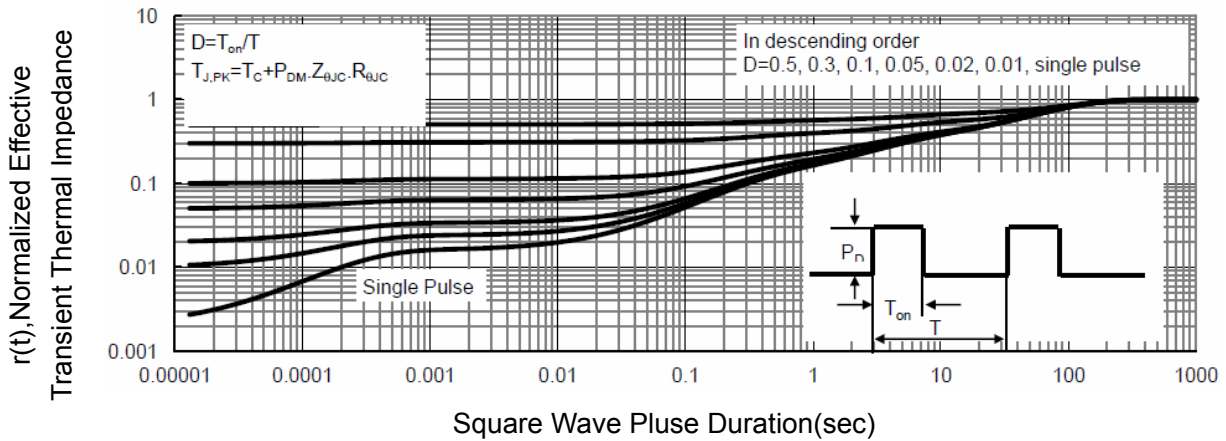
Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area



T_J-Junction Temperature(°C)

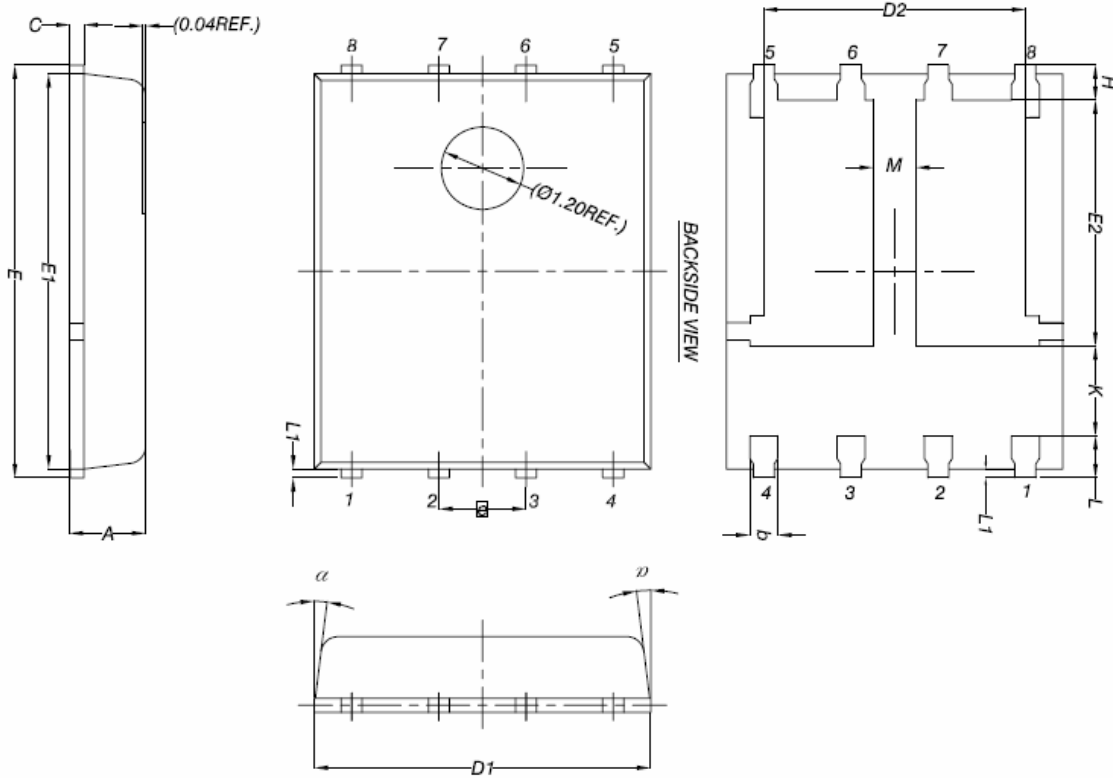
Figure 14 BV_{DSS} vs Junction Temperature



Square Wave Pulse Duration(sec)

Figure 15 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

Attention:

- Any and all NCE power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your NCE power representative nearest you before using any NCE power products described or contained herein in such applications.
- NCE power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all NCE power products described or contained herein.
- Specifications of any and all NCE power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- NCE power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all NCE power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of NCE power Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. NCE power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the NCE power product that you intend to use.
- This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.